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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Sunil Talwar et al.

Title: MULTIPLICATION LOGIC CIRCUIT

Docket No.: 1365.048US1

Filed: July 3, 2001

Examiner:

Serial No.: 09/898,752

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Group Art Unit: 2121

Commissioner for Patents  
Washington, D.C. 20231

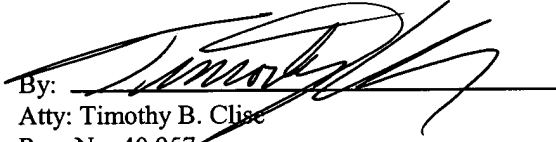
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We are transmitting herewith the following attached items (as indicated with an "X"):

- ☒ A return postcard.
- ☒ An Information Disclosure Statement (2 pgs.), Form 1449 (2 pgs.), and copies of 19 cited references.
- ☒ UK Search Reports (4 pgs.).

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SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.  
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CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner for Patents, Washington, D.C. 20231, on this 19<sup>th</sup> day of February, 2002.

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